AMENDMENTS TO THE SPECIFICATION:

Please amend the paragraph beginning on page 1, line 26 as follows:

CPU 112 mainly controls the entire system while accessing memory [[111]]

111a. Logic 114 performs processing of an operation such as a floating-point

operation while accessing memory 111b. DSP 113 performs data processing while

accessing memory 111c. CPU 112 receives an operation result from logic 114 and a

data processing result from DSP 113 through the system bus to control the system

entirely.